

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	40	((silicon semiconductor) adj2 (wafer backplane) microdisplay) and (spacerless free adj2 spacer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/18 16:26
L2	10	((silicon semiconductor) adj2 (wafer backplane) microdisplay) and seal\$3 with conduct\$3 adj2 spacer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/18 16:30
L3	781	349/153	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/18 16:30
L4	16	3 and conduct\$3 adj2 spacer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/18 16:30